

POWER SUPPLY CONTROL WITH BUILT-IN WATCHDOG TIMER

■ DESCRIPTION

TK1232 is designed to monitor power supply within the system of reset signal generation for microprocessors. It is used in monitor systems for controlling various processes and entities. Packaged in 8-pin SOIC.

■ FEATURES

- Rated supply voltage 5.0 V
- Accurate 5% or 10% microprocessor power supply monitoring
- Programming of watchdog timer overflow time
- Generation of reset signals at power on for correct microprocessor start.

The chip contains reference voltage source, analog comparator, Watchdog timer, circuit for monitoring power supply deviation accuracy.

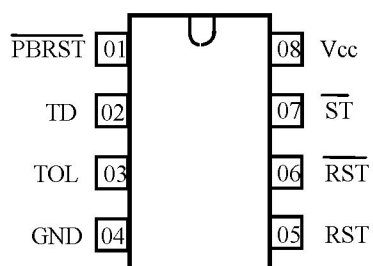
■ FUNCTIONS

- Reset signal generation after power failure/ error
- Reset signal generation from external “RESET” pushbutton
- Reset signal generation from watchdog timer

■ ORDERING INFORMATION

Part Number	Package	Packing	Temperature	Package Qty
TK1232LPS	SOIC-8	Reel	0°C ~ 70°C (TA)	2500
TK1232LPSN	SOIC-8	Reel	-40°C ~ 85°C (TA)	2500
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■ PIN CONFIGURATION



■ PIN DESCRIPTION

Pin	Symbol	Description
01	$\overline{\text{PBRST}}$	Pushbutton reset input
02	TD	Time Delay Set
03	TOL	Selects 5% or 10% V_{CC} Detect
04	GND	Ground
05	RST	Reset output (Active High)
06	$\overline{\text{RST}}$	Reset output (Active Low, open drain)
07	$\overline{\text{ST}}$	Strobe Input
08	V_{CC}	Supply output from voltage source

■ ABSOLUTE MAXIMUM RATING

Absolute maximum ratings

Symbol	Parameter	Typical		Units
		Min	Max	
V_{CC}	Supply voltage	-	7.0	V
V_{IH}	Input voltage, high level	-	7.0	V
V_{IL}	Input voltage, low level	-1.0	-	V
T_A	Operating temperature range	-40	+85	°C
T_{stg}	Storage temperature	-60	+125	°C

These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied.

Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

■ ELECTRICAL CHARACTERISTICS

Recommended operating conditions

Symbol	Parameter	Typical		Units
		Min	Max	
V_{CC}	Supply voltage	4.5	5.5	V
V_{IH}	Input voltage, high level	2.0	$U_{CC}+0.3$	V
V_{IL}	Input voltage, low level	-0.3	0.8	V
T_A	Operating temperature range	-20	+70	°C

DC electrical characteristics ($T_{Amb} = -40^{\circ}$ to $+85^{\circ}\text{C}$)

Symbol	Parameter	Test conditions	Typical		Units
			min	max	
I_{LIL1}	Input leakage current, low level, ST, TOL	$V_{CC}=5\text{ V}\pm 10\%$, $V_{IL}=0\text{ V}$	-	-1	μA
I_{LIL2}	Input leakage current, low level, TD	$V_{CC}=5\text{ V}\pm 10\%$, $V_{IL}=0\text{ V}$	-	-300	μA
I_{LIL3}	Input leakage current, low level, PBRST	$V_{CC}=5\text{ V}\pm 10\%$, $V_{IL}=0\text{ V}$	-	-1000	μA
I_{LIH1}	Input leakage current, high level, ST, TOL	$V_{CC}=5\text{ V}\pm 10\%$, $V_{IH}=V_{CC}$	-	1	μA
I_{LIH2}	Input leakage current, high level, TD	$V_{CC}=5\text{ V}\pm 10\%$, $V_{IH}=V_{CC}$	-	300	μA
I_{OH}	Output current, high level, RST	$V_{CC}=5\text{ V}\pm 10\%$, $V_{OH}=2.4\text{ V}$	-8	-	μA
I_{OL}	Output current, low level, RST, RST	$V_{CC}=5\text{ V}\pm 10\%$, $V_{OL}=0.4\text{ V}$	8	-	mA
V_{OH}	Output voltage, high level, RST	$V_{CC}=5\text{ V}\pm 10\%$, $I_{OH}=-500\text{ }\mu\text{A}$	$V_{CC}-0.5$	-	V
V_{OH1}	Output voltage, high level, - RST	$V_{CC}=2\text{ V}$, $I_{OH}=-500\text{ }\mu\text{A}$	$V_{CC}-0.5$	-	V
V_{OL}	Output voltage, low level, RST	$V_{CC}=2\text{ V}$, $I_{OL}=1\text{ mA}$	-	0.4	V
I_{CC}	Operating current	$V_{CC}=5\text{ V}\pm 10\%$	-	2	mA
$V_{CC\text{ TP1}}$	V_{CC} trip point	TOL = GND	4.5	4.74	V
$V_{CC\text{ TP2}}$	V_{CC} trip point	TOL = V_{CC}	4.25	4.49	V

AC electrical characteristics ($T_{amb} = \text{from } -40 \text{ to } +85^{\circ}\text{C}$)

Symbol	Parameter	Test conditions	Typical		Units
			min	max	
t_{TD1}	Watchdog timer overflow time	$V_{CC}=5.0\text{ V}\pm 10\%$ $t_{ST}\geq 20\text{ ns}$ TD = GND	62.5	250	ms
t_{TD2}		TD disconnected	250	1000	ms
t_{TD3}		TD = V_{CC}	500	2000	ms
t_{PDLY}	PBRST stable low to RST and RST	$V_{CC}=5.0\text{ V}\pm 10\%$ $t_{PB}\geq 20\text{ ms}$	-	20	ms
t_{RST}	Reset active time	$V_{CC}=5.0\text{ V}\pm 10\%$ $t_{PB}\geq 20\text{ ms}$	250	1000	ms
t_{RPD}	V_{CC} fail detect to RST and RST	$V_{CC}=\text{from } 5.0 \text{ to } 4.0\text{ V}$ $t_F\geq 10\text{ }\mu\text{s}$	-	175	μs
t_{RPU}	V_{CC} detect to RST and RST transition	$V_{CC}=\text{from } 5.0 \text{ to } 4.0\text{ V}$ $t_R\geq 1\text{ }\mu\text{s}$	250	1000	ms

■ TIMING DIAGRAM:

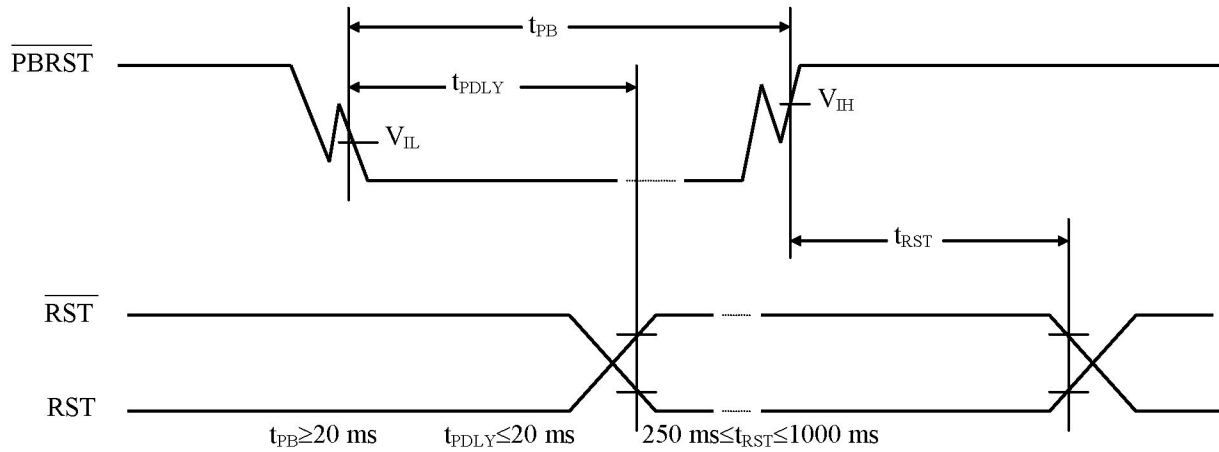


Fig. 2 – Timing diagram of forming reset signal from external PBRST control button

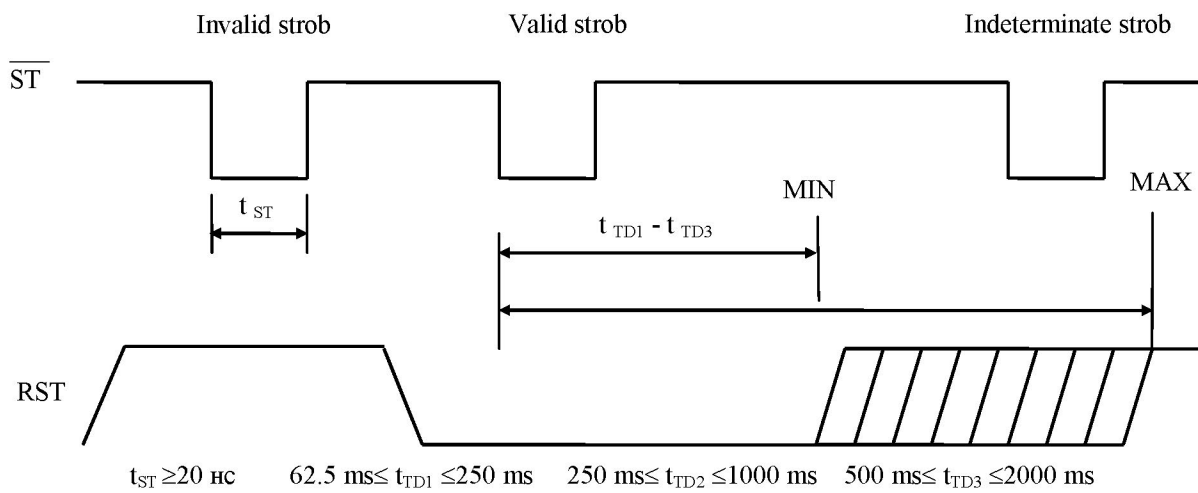
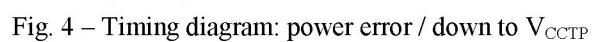


Fig. 3 – Timing diagram : Strobe input



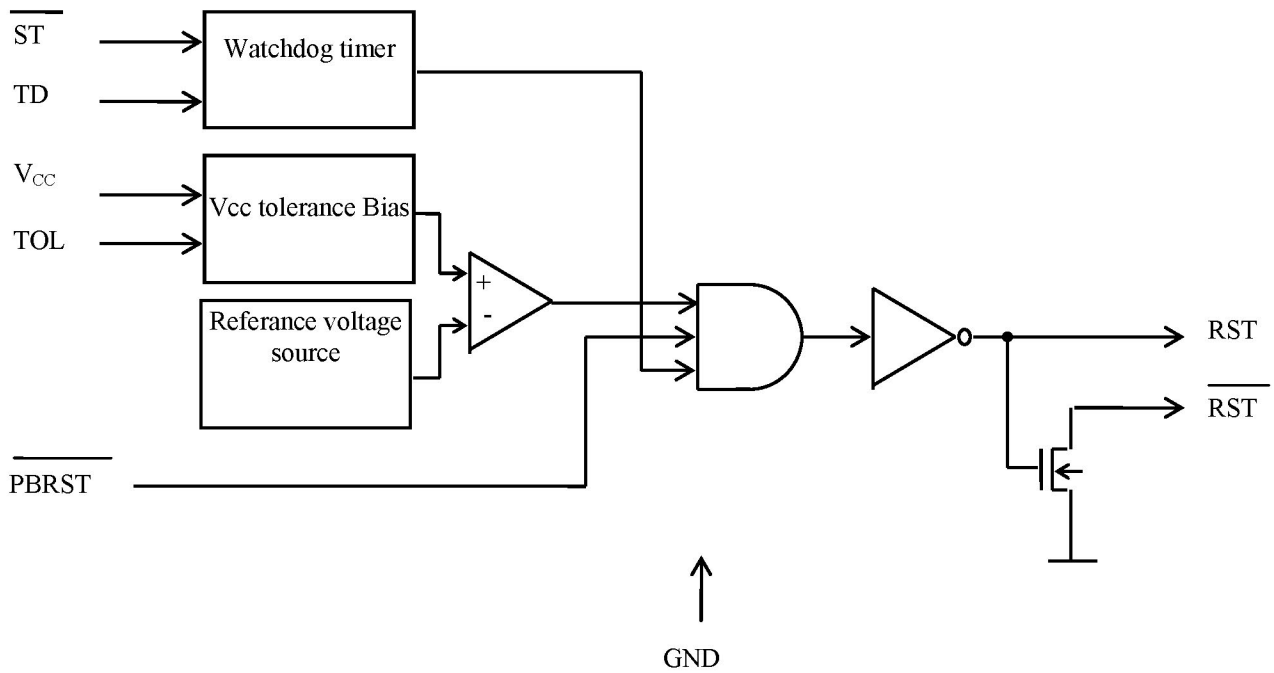


Fig.6 Block diagram

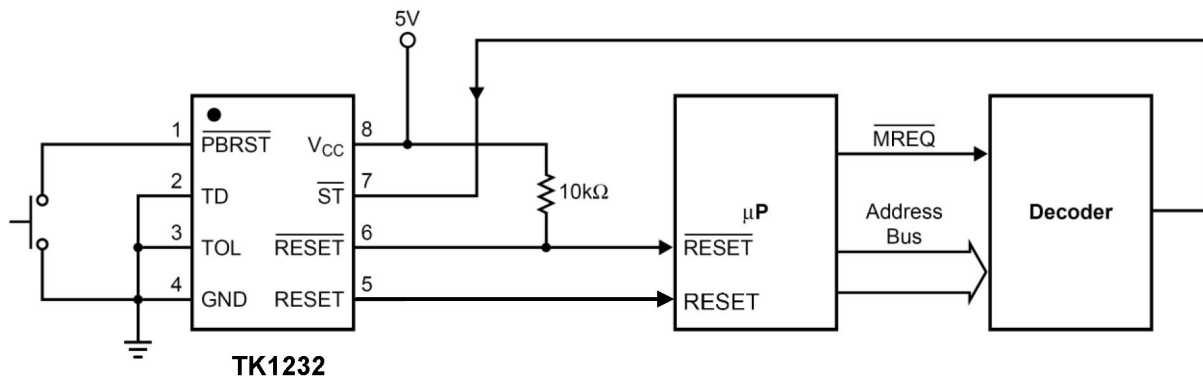
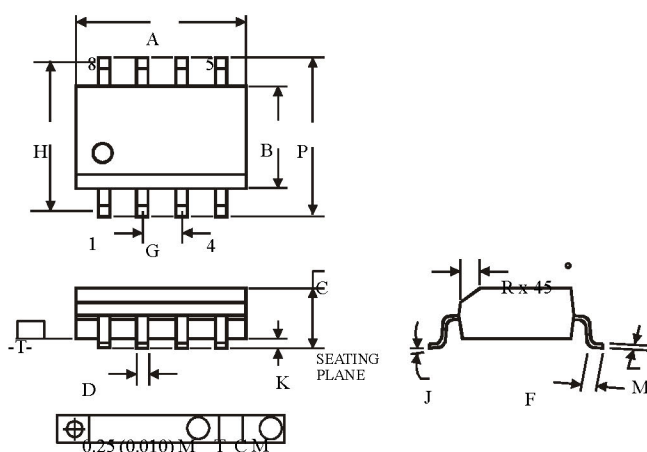


Fig.7 Application Circuit : Watchdog Timer

■ Package Dimensions

SOIC 8



NOTES:

1. Dimensions A and B do not include mold flash or protrusion.
2. Maximum mold flash or protrusion 0.15 mm (0.006) per side for A; for B = 0.25 mm (0.010) per side.

Symbol	Dimension, mm	
	MIN	MAX
A	4.8	5
B	3.8	4
C	1.35	1.75
D	0.33	0.51
F	0.4	1.27
G	1.27	
H	5.72	
J	0°	8°
K	0.1	0.25
M	0.19	0.25
P	5.8	6.2
R	0.25	0.5